Weekly Progress Report

Week ending 11/2/2014

Completed Tasks:

1. Helped debug the prototype circuit with team, incorporated sub-circuits and code.
2. Finalized Schematic, added final component selections and test points.
3. Created BOM with the finalized schematic, and ordered Parts.
4. Created first layout of the PCB Board design.

Tasks to Be Completed Next Week:

1. Print out scale version of board layout and compare components.
2. Finalize 1st version of board layout, and router out prototype board.
3. Create documentation lock operation algorithm.